

**CRYSTAL OSCILLATOR
LOW-JITTER SAW OSCILLATOR**

EG-4121 / 4101CA

- Frequency range : 100 MHz to 700 MHz
- Supply voltage : 2.5 V ... EG-4121CA
3.3 V ... EG-4101CA
- Output : Differential LV-PECL or LVDS or HCSL
- Function : Output enable (OE)
- External dimensions : 7.0 × 5.0 × 1.2 mm
- Very low jitter and low phase noise by SAW unit.


**Product Number (please contact us)
X1M0001x1xxxx00**


Actual size

EG-4121CA

EG-4101CA


Specifications (characteristics)
► Differential LV-PECL Output

| Item | Symbol | EG-4121CA P | EG-4101CA P | Conditions / Remarks |
|-----------------------------|---------------------------------|---|---------------|--|
| | | Differential LV-PECL | | |
| Output frequency range | f _o | 100 MHz to 700 MHz | | Please contact us about available frequencies. |
| Supply voltage | V _{cc} | 2.5 V ±0.125 V | 3.3 V ±0.33 V | |
| Storage temperature | T _{stg} | -55 °C to +125 °C | | Storage as single product. |
| Operating temperature *1 | T _{use} | W: -40 °C to +85 °C | | |
| Frequency tolerance *1 | f _{tol} | G: ± 50 × 10 ⁻⁶ | | |
| Current consumption | I _{cc} | 60 mA Max. | | OE=V _{cc} , L_ECL=50 Ω |
| Disable current | I _{dis} | 2 mA Max. | | OE=GND |
| Symmetry | SYM | 45 % to 55 % | | at outputs crossing point |
| Output voltage | V _{OH} | 1.55 V Typ. | 2.35 V Typ. | DC characteristics |
| | V _{OL} | 0.8 V Typ. | 1.6 V Typ. | |
| | | V _{cc} -1.025 V to V _{cc} -0.88 V | | |
| | | V _{cc} -1.81 V to V _{cc} -1.62 V | | |
| Output load condition (ECL) | L _{ECL} | 50 Ω | | Terminated to V _{cc} -2.0 V |
| Input voltage | V _{IH} | 70 % V _{cc} Min. | | OE terminal |
| | V _{IL} | 30 % V _{cc} Max. | | |
| Rise time / Fall time | t _r / t _f | 400 ps Max. | | Between 20 % and 80 % of (V _{OH} -V _{OL}) |
| Start-up time | t _{str} | 10 ms Max. | | Time at minimum supply voltage to be 0 s |
| Phase Jitter | t _{pj} | 0.23 ps Max. | | 100 MHz ≤ f _o < 150 MHz |
| | | 0.22 ps Max. | | 150 MHz ≤ f _o < 200 MHz |
| | | 0.21 ps Max. | | 200 MHz ≤ f _o < 300 MHz |
| | | 0.18 ps Max. | | 300 MHz ≤ f _o < 400 MHz |
| | | 0.16 ps Max. | | 400 MHz ≤ f _o < 500 MHz |
| | | 0.14 ps Max. | | 500 MHz ≤ f _o < 600 MHz |
| | | 0.10 ps Max. | | 600 MHz ≤ f _o ≤ 700 MHz |
| | | | | Offset frequency: 12 kHz to 20 MHz |

*1 As per table 1 below.

► LVDS Output

| Item | Symbol | EG-4121CA L | EG-4101CA L | Conditions / Remarks |
|------------------------------|---------------------------------|--------------------------------|---------------|--|
| | | LVDS | | |
| Output frequency range | f _o | 100 MHz to 700 MHz | | Please contact us about available frequencies. |
| Supply voltage | V _{cc} | 2.5 V ±0.125 V | 3.3 V ±0.33 V | |
| Storage temperature | T _{stg} | -55 °C to +125 °C | | Storage as single product. |
| Operating temperature *1 | T _{use} | W: -40 °C to +85 °C | | |
| Frequency tolerance *1 | f _{tol} | G: ± 50 × 10 ⁻⁶ | | |
| Current consumption | I _{cc} | 30 mA Max | | OE=V _{cc} , L_LVDS=100 Ω |
| Disable current | I _{dis} | 15 mA Max | | OE=GND |
| Symmetry | SYM | 45 % to 55 % | | at outputs crossing point |
| Output voltage | V _{OD} | 350 mV Typ. 247 mV to 454 mV | | DC characteristics |
| | dV _{OD} | 50 mV Max. | | |
| | V _{OS} | 1.25 V Typ. 1.125 V to 1.375 V | | |
| | dV _{OS} | 150 mV Max. | | |
| Output load condition (LVDS) | L _{LVDS} | 100 Ω | | Connected between OUT to OUT |
| Input voltage | V _{IH} | 70 % V _{cc} Min. | | OE terminal |
| | V _{IL} | 30 % V _{cc} Max. | | |
| Rise time / Fall time | t _r / t _f | 400 ps Max. | | Between 20 % and 80 % of Differential Output Peak to Peak voltage. |
| Start-up time | t _{str} | 10 ms Max. | | Time at minimum supply voltage to be 0 s |
| Phase Jitter | t _{pj} | 0.27 ps Max. | | 100 MHz ≤ f _o < 150 MHz |
| | | 0.24 ps Max. | | 150 MHz ≤ f _o < 200 MHz |
| | | 0.23 ps Max. | | 200 MHz ≤ f _o < 300 MHz |
| | | 0.19 ps Max. | | 300 MHz ≤ f _o < 400 MHz |
| | | 0.16 ps Max. | | 400 MHz ≤ f _o < 500 MHz |
| | | 0.14 ps Max. | | 500 MHz ≤ f _o < 600 MHz |
| | | 0.10 ps Max. | | 600 MHz ≤ f _o ≤ 700 MHz |
| | | | | Offset frequency: 12 kHz to 20 MHz |

*1 As per table 1 below.



► HCSL Output

| Item | Symbol | EG-4121CA H | | EG-4101CA H | | Conditions / Remarks |
|------------------------------|---------------------------------|----------------------------|--|--------------|--|--|
| | | HCSL | | | | |
| Output frequency range | fo | 100 MHz to 350 MHz | | | | Please contact us about available frequencies. |
| Supply voltage | Vcc | 2.5 V ±0.125 V | | 3.3 V ±0.3 V | | |
| Storage temperature | T_stg | -55 °C to +125 °C | | | | Storage as single product. |
| Operating temperature | T_use | W: -40 °C to +85 °C | | | | |
| Frequency tolerance *1 | f_tol | G: ± 50 × 10 ⁻⁶ | | | | |
| Current consumption | Icc | 75 mA Max. | | 85 mA Max. | | OE=Vcc, L_HCSL=50 Ω |
| Disable current | I_dis | 20 mA Max. | | 35 mA Max. | | OE=GND |
| Symmetry | SYM | 45 % to 55 % | | | | at outputs crossing point |
| Output Voltage | V _{OH} | 0.75 V Typ. | | | | DC characteristics |
| | V _{OL} | -0.3 V Typ. | | | | |
| Output load condition (HCSL) | L_HCSL | 50 Ω | | | | Terminated to GND |
| Input voltage | V _{IH} | 70 % Vcc Min. | | | | OE terminal |
| | V _{IL} | 30 % Vcc Max. | | | | |
| Rise time / Fall time | t _r / t _f | 500 ps Max. | | | | Between 0.175 V and 0.525 V of output |
| Start-up time | t_str | 10 ms Max. | | | | Time at minimum supply voltage to be 0 s |
| | | 0.8 ps Max. | | | | |
| Phase Jitter | t _{pj} | 0.5 ps Max. | | | | Offset frequency: 12 kHz to 20 MHz |
| | | 100 MHz ≤ fo < 200 MHz | | | | |
| | | 200 MHz ≤ fo | | | | |

*1 As per table 1 below.

Table 1 Frequency tolerance and aging

| Output | | P: Differential LV-PECL | L: LVDS | H: HCSL |
|---|--|-------------------------|---------|---------|
| Aging | | A *2 | A *2 | A *2 |
| Frequency tolerance and Operating temperature | GW: ±50 × 10 ⁻⁶ (-40°C to +85°C) | PGWA | LGWA | HGWA |

*2 This includes initial frequency tolerance, temperature variation, supply voltage variation, reflow drift, and aging(+25 °C, 10 years).

Table 2 Jitter

| Item | Symbol | Specifications | Remarks |
|----------|------------------|----------------|---|
| Jitter * | t _{DJ} | 0.2 ps Typ. | Deterministic Jitter |
| | t _{RJ} | 3 ps Typ. | Random Jitter |
| | t _{RMS} | 2 ps Typ. | σ (RMS of total distribution) |
| | t _{p-p} | 20 ps Typ. | Peak to Peak |
| | t _{acc} | 4 ps Typ. | Accumulated Jitter(σ) n=2 to 50000 cycles |

* Tested using a DTS-2075 Digital timing system made by WAVECREST with jitter analysis software VISI6.

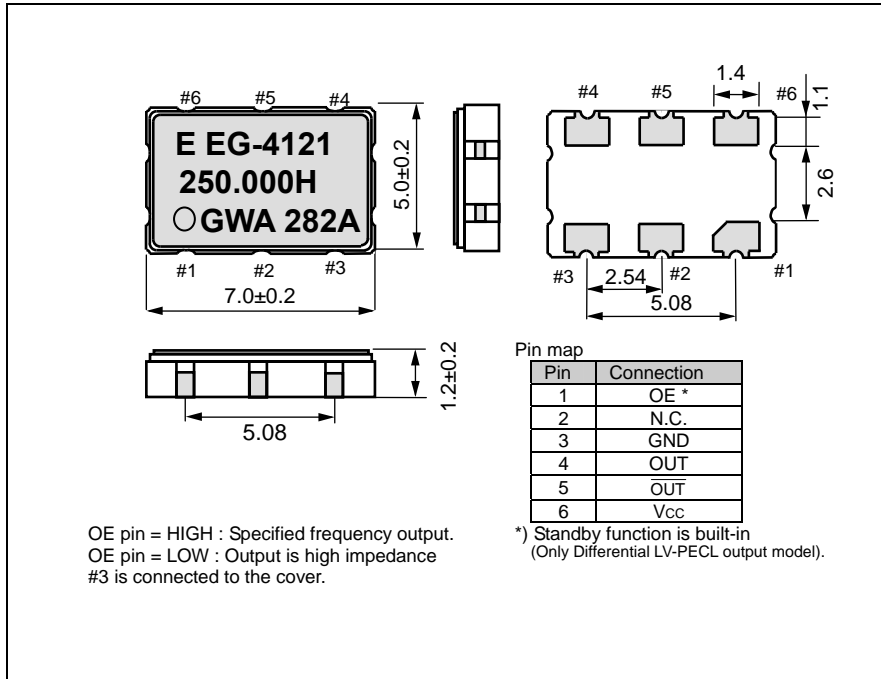
: Differential LV-PECL, LVDS output

* Based on SIA-3100C signal integrity analyzer made from WAVECREST.

: HCSL output

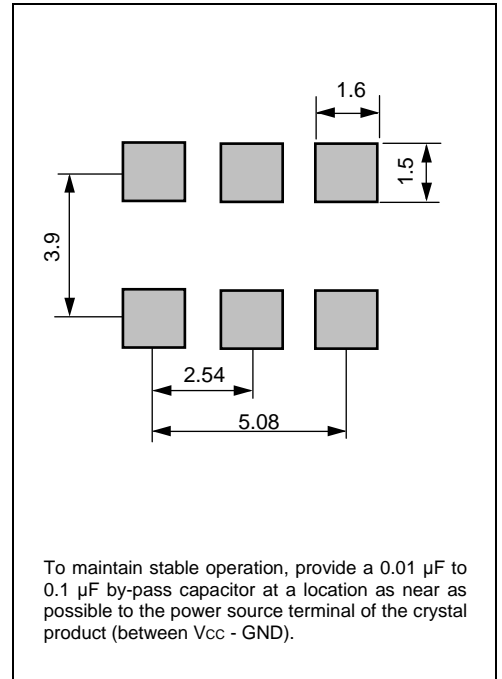
External dimensions

(Unit:mm)



Footprint (Recommended)

(Unit:mm)



PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.




WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs,

Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired ISO/TS 16949 certification that is requested strongly by major automotive manufacturers as standard.

ISO/TS16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

► Explanation of the mark that are using it for the catalog

| | |
|---|---|
|  | <p>► Pb free.</p> |
|  | <p>► Complies with EU RoHS directive. *About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive. (Contains Pb in sealing glass, high melting temperature type solder or other.)</p> |
|  | <p>► The products have been designed for high reliability applications such as Automotive.</p> |

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